

# HOLE PLUGGING METHOD FOR PRINTED CIRCUIT BOARDS, AND HOLE PLUGGING DEVICE

## BACKGROUND OF THE INVENTION

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### 1. Field of the Invention

20446-0140  
The present invention relates to a hole plugging method for printed circuit boards, a hole plugging device which carries out such method and a manufacturing method in accordance, and particularly, to a hole plugging method for printed circuit board and to a hole plugging device and a manufacturing method in accordance therewith which plugs holes in a printed circuit board with an insulating resin or solder resist by abutting a squeegee or doctor blade to an exposed hole using a mask capable of exposing a certain portion of a board or plugs resin or solder resist between circuit patterns with.

### 2. Description of the Background Art

Recently, integrated circuits (IC) have become more densified by an increase in the degree of integration according to the development of technology. Accordingly, it is required that a printed circuit board for mounting ICs become more densified.

For meeting this necessity, currently, the printed circuit board is densified by fabricating a multi layer (printed circuit) board formed with a plurality of layers. In the multi layer board 1, open through holes 1d and via blind holes 1c may be

form in the board wherein a conductive layer 1e is coated in the holes 1c and 1d to electrically interconnect circuit patterns 1a and 1b of one or more an inner layers and/or the surface layers, with reference to Figure 1A.

The through hole 1d has a relatively large diameter than the via hole 1c and is typically formed by employing a mechanical process such as by drilling. However, the interval between the through holes 1d can not be narrowed beyond a certain spacing and there is thus a limitation in the densification degree of the board when the sizes of tools and avoidance of damage to the board 1 in the processing are taken into consideration.

Based on the above characteristics, in the case of forming a via hole 1c with a small diameter by employing a chemical method such as by etching, the interval between adjacent via holes 1c can be made narrower than that between the through holes 1d, and a circuit pattern can be formed at the portion adjacent the via hole 1c and the upper side of the via hole 1c, thus to achieve higher integration and miniaturization of the printed circuit board.

Such a via hole 1c has a conductive layer 1e and is coated with a solder resist using a screen printing process or a roller coating process to protect the conductive layer 1e from external impact and to prevent foreign materials from attaching to the conductive layer 1e.

First, the conventional hole plugging method of the printed circuit board using the screen printing process will be described with reference to Figures 1A and 1B.

The structure of the printed circuit board includes circuit patterns (surface circuits patterns) 1a positioned on both the upper and lower surfaces of the board 1 and circuit patterns (internal circuits) 1b positioned in the inner layers of the

board.

On the board 1, a via holes 1c is formed to electrically interconnect the surface circuit pattern 1a and internal circuit 1b through the conductive layer 1e, and the through hole 1d is formed to electrically interconnect both the upper and lower surface circuit patterns 1a through the conductive layer 1e.

In the hole plugging method of the printed circuit board using the screen printing technique, the board 1 is positioned on a jig (not shown) and a screen 2 such as a mesh is positioned at the upper side thereof at a certain distance.

Then, after coating solder resist (SR) on the upper surface of the screen 2, the solder resist (SR) is discharged through an opening 2a in the screen 2 by pushing a squeegee 3 in the direction of the arrow in the drawing and then is filled in the via hole 1c and through hole 1d of the board 1.

In the above screen printing technique, the screen 2 is formed in the form of a mesh by weaving a plurality of fine wires 2b crossing each other. The wire diameter of a #110 screen is approximately 0.08mm and the wire diameter of a #600 screen is approximately 0.045mm. As shown in Figure 1B, if that the diameter of the subminiature via hole is 0.1 to 0.15mm, in the portion where the fine wires intersect, part of the via hole 1c is covered since the diameter of the conventional fine wire 2b is half that of the via hole 1c.

Particularly, a considerable portion of the via hole 1c where the fine wires 2b cross each other is covered and accordingly, the solder resist (SR) cannot be plugged sufficiently.

Also, the solder resist (SR) is coated in the space 1f between the surface circuit pattern 1a and the screen 2 since the solder resist SR is coated by using the screen 2. However, removing the solder resist SR afterwards by an abrasive

process to expose the surface circuit pattern 1a is not easy since the solder resist SR is coated covering the surface circuit pattern 1a completely and damages may result to the surface circuit pattern 1a in case of removing the solder resist SR by using a brush and the like.

5       Second, the conventional hole plugging method of the printed circuit board using the roller coating technique will be described with reference to Figures 2A and 2B.

10       In the hole plugging method using the roller coating after supplying the solder resist SR to an upper roller 4a and lower roller 4b, the solder resist SR at the rollers 4a and 4b is transferred to the board 1 and coated by passing the board 1 between rollers 4a and 4b at the both side thereof.

15       In the conventional roller coating method, the solder resist SR is coated by putting the solder resist SR in a groove (not shown) formed on the circumferential surface of the rollers 4a and 4b, but in case a projecting portion 5 with relatively small amount of solder resist SR is abutted to the via hole 1c or blocks up the via hole 1c, a sufficient amount of solder resist SR can not be plugged.

20       In the screen printing method, the solder resist SR is coated in the via hole 1c or the through hole 1d of the board 1 by pushing the solder resist SR coated on the screen 2 after putting the screen 2 on the board 1. In the roller coating method, the solder resist SR on the rollers 4a and 4b is flowed and coated on the via hole 1c and the through hole 1d of the board 1. Air bubbles remaining behind in the solder resist SR are discharged due to the high heat when the integrated circuit (not shown) is mounted, and accordingly the circumferential circuit patterns, solder resist and integrated circuit can be damaged even though the solder resist SR is  
25       filled in the via hole 1c and the through hole 1d. Also, the coefficients of thermal

expansion of the integrated circuit and the board are different and accordingly, the board can be damaged when high heat is generated in the integrated circuit.

To solve the above problems, the present applicant had filed an earlier application for "a method for fabricating a printed circuit board using vacuum" (Korea Patent Application No. 2000-19045) on April 11, 2000 and also an application for us Letters patent therefore, under US Patent application No. 09/832,122.

In the above fabrication method, the solder resist is coated on the via hole of the board, and at the same time air in the via hole is discharged to the outside and the solder resist is plugged in the via hole. The solder resist can be plugged in the via hole efficiently by completely removing the residual air bubbles in the via hole. However, there are disadvantages in the cost and the manufacturing time, in that a vacuum device must be equipped and repeated operation of exposure under the vacuum condition and plugging operations are needed thus to reduce productivity.

## SUMMARY OF THE INVENTION

Therefore, an object of the present invention is to provide a hole plugging method for printed circuit boards and a hole plugging device in accordance therewith, capable of smoothly plugging a solder resist insulating material in a via hole formed to electrically connect a circuit pattern formed on the surface of the board and a circuit pattern formed in the board, or in a through hole formed to electrically connect the circuit patterns on the both side surfaces of the board.

Another object of the present invention is to provide a hole plugging

method for printed circuit boards and a hole plugging device in accordance therewith capable of plugging a resin insulating material by coating the resin in spaces between the circuit patterns just to the height of the circuit patterns and at the same time, minimizing the amount of insulating material coated on the circuit pattern.

Still another object of the present invention is to provide a hole plugging method for printed circuit boards and a hole plugging device in accordance therewith capable of preventing bubbles from remaining behind in the insulating material plugged in the spaces between via holes, through holes or circuit patterns on the board.

Still another object of the invention is to provide a hole plugging method for printed circuit boards and a hole plugging device in accordance therewith capable of preventing an integrated circuit on the board from being damaged by restraining the formation of residual bubbles in the insulating material in the spaces between the via holes, through holes or circuit patterns on the board sufficiently.

To achieve these and other advantages and in accordance with the purpose of the present invention, as embodied and broadly described herein, there is provided a hole plugging method for printed circuit boards in which an insulating material is plugged in a via hole by abutting a squeegee for plugging an insulating material in the via hole directly to a surface of a board where the via hole is formed to electrically connect a circuit pattern formed on the surface of the board and a circuit pattern formed in the board.

Also, to achieve the above object, there is provided a hole plugging method for printed circuit boards, wherein an insulating material is plugged in

exposed via holes by positioning a mask for selectively exposing the via holes on the board where the via hole is formed to connect a circuit pattern formed on the surface of the board and a circuit pattern formed in the board electrically.

Also, to achieve the above object, the hole plugging method for the printed circuit boards in accordance with the present invention pushes and plugs the insulating material into the spaces between the circuit patterns by abutting the squeegee for plugging the insulating material in the spaces between the circuit patterns formed on the surface of the board to the surface of the board and the circuit patterns.

Also, to achieve the above object, in the hole plugging method for the printed circuit boards, a mask for selectively exposing the insulating material in the spaces between the circuit patterns to plug the insulating material in the spaces among the circuit patterns formed on the surface of the board is positioned on the board and the insulating material is plugged only in the spaces between the exposed circuit patterns.

To achieve the above object, the hole plugging method for the printed circuit boards in accordance with the present invention includes the steps of providing the mask exposing the via hole and covering another portion on the board, the board having the via hole for connecting the circuit pattern to connect the circuit pattern formed on the surface of the board and the circuit pattern formed in the board electrically, positioning the mask on the board and plugging the insulating material in the via hole or in the spaces between the surface circuit patterns by coating the insulating material on the portion of the board exposed by the mask.

Also, to achieve the above object, the hole plugging method for the printed

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circuit boards in accordance with the present invention includes the steps of providing the mask exposing the via hole and covering another portion on the board, the board having the via hole for electrically connecting the circuit pattern formed on the surface of the board and the circuit pattern formed in the board and the through hole for electrically connecting the circuit patterns of the both side surfaces, positioning the mask on the board and plugging the insulating material in the via hole by coating the insulating material on the portion of the board exposed by the mask.

To achieve the above object, the hole plugging device for the printed circuit boards in accordance with the present invention includes a mask positioned on the board where the via hole is formed to connect the circuit pattern formed on the surface of the board and the circuit pattern formed in the board electrically, and a pressing means for plugging the insulating material in the via hole by coating the insulating material on the surface of the board exposed by the mask.

On the other hand, to achieve the above object, in the hole plugging method for the printed circuit boards in accordance with the present invention, solder resist or insulating resin is filled in the spaces between surface side circuit patterns by moving the squeegee under the condition of being abutted on the upper surface of the circuit pattern on the surface of the printed circuit board, the board having a plurality of circuit patterns formed thereon and formed in the board and a hole for electrically connecting the circuit pattern formed on the surface of the board and the circuit pattern formed in the board or for connecting the circuit patterns on the both side surfaces and by moving the squeegee being abutted to the surface of the printed circuit board.

The foregoing and other objects, features, aspects and advantages of the



present invention will become more apparent from the following detailed description of the present invention when taken in conjunction with the accompanying drawings.

## 5 BRIEF DESCRIPTION OF THE DRAWINGS

20 The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate embodiments of the invention and together with the description serve to explain the principles of the invention.

In the drawings:

Figure 1A is a cross-sectional view showing a hole plugging method for printed circuit boards using a conventional screen printing method;

15 Figure 1B is a plan view briefly showing a hole plugging process for printed circuit boards using the conventional screen printing method;

Figure 2A is a cross-sectional view briefly showing the hole plugging for printed circuit boards using a conventional roller coating method;

Figure 2B is an enlarged cross-sectional view showing the hole plugging process for the printed circuit boards using the conventional roller coating method;

20 Figure 3 is a perspective view showing a hole plugging device for printed circuit boards according to an embodiment of the present invention;

Figure 4 is a cross-sectional view showing the hole plugging device and method for printed circuit boards according to the embodiment of the present invention;

25 Figure 5 is a detail view showing a state where a hole is plugged in a

printed circuit board according to the embodiment of the present invention;

Figure 6 is a cross-sectional view showing the hole plugging method for printed circuit boards according to another embodiment of the present invention; and

5 Figure 7 is a cross-sectional view showing the hole plugging method for printed circuit boards according to another embodiment of the present invention.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

10 Reference will now be made in detail to the preferred embodiments of the present invention, examples of which are illustrated in the accompanying drawings.

Figure 3 is a perspective view showing a hole plugging device for printed circuit boards according to an embodiment of the present invention, Figure 4 is a cross-sectional view showing the hole plugging device and method for printed circuit boards according to the embodiment of the present invention and Figure 5 is a detail view showing a state where a hole is plugged in a printed circuit board according to the embodiment of the present invention.

15 Firstly, the printed circuit board includes a surface circuit pattern 1a and internal circuit conductors 1b are formed on and in a board 1 respectively, a via hole 1c formed to electrically connect the surface circuit pattern 1a and the internal circuit pattern 1b by a conductive layer 1e, and a through hole 1d formed to connect the both side surface circuit patterns 1a by the conductive layer 1e.

20 The hole plugging device for a printed circuit board in accordance with the present invention includes a mask 10 wherein a opening portion 11 is formed to selectively expose the via holes 1c, through holes 1d and the surface circuit

patterns 1a under the condition of being positioned on the board 1 and a squeegee 20 which serves as a pressing implement, by being directly abutted on the exposed part of the board 1 exposed by the mask 10 to coat a resin or solder resist SR thereon, which are insulating materials.

5 It is desirable that the mask 10 is made of a metal material having a certain extent of tension to correspond to stress generated when the squeegee 20 is given pressure, but the mask can be formed with a mixed material of a fibrous material and a resinous material or of a film type member.

Also, the mask 10 includes an opening portion or portions 11 formed by  
10 cutting away portions thereof corresponding in location to areas to be exposed on the board 1 among the via holes 1c and the through holes 1d of the board 1, as with a laser and the like, and a masking portion 12 which does not expose the board 1.

The process of plugging resin or solder resist in the via holes and the  
15 through holes of the printed circuit board using the hole plugging device in accordance with the present invention will be described as follows.

As shown in Figure 4, in case the board has a circuit pattern as a four layer structure by independently forming the surface circuit patterns 1a on the both upper and lower surfaces of the board 1 and forming the internal circuits 1b in the  
20 board 1 as a two layer structure, the surface of the conductive layer 1e is coated with resin or solder resist to protect the conductive layer 1e since the via hole 1c and the through hole 1d have the conductive layers 1e which are metal layers to conduct electricity.

The mask 10 is positioned by having the opening portion 11 in the mask  
25 10 correspond to the via hole 1c to expose the via hole 1c of the board 1.

The element designated by reference numeral 6 and indicated with dotted lines in Figure 3 is a circuit area of the board 1 corresponding to the opening portion 11. Therefore, the via hole 1c of the circuit area 6 is exposed by the opening portion 11 and the other areas on the board 1 are masked by the masking portion 12.

Next, when the squeegee 20 is lowered and the squeegee is abutted on the mask 10 and if the squeegee is pressed down more, the squeegee 20 is abutted on the mask and through opening 11 as the end of the blade of the squeegee is bent.

The resin or solder resist SR is plugged in the via hole 1c by moving the squeegee 20 in the direction of the arrow as shown in Figure 4.

Namely, as the lower end of the blade of squeegee 20 passes over the surface of the masking portion 12 of the mask 10, the squeegee 20 pushes the resin or solder resist SR positioned on the surface. Then, as the squeegee 20 is progressed while being abutted on the upper surface of the board 1 by positioned at the opening portion 11 of the mask 10, the squeegee plugs the resin or resister SR into the via hole 1c by pushing the resin or solder resist SR while being positioned on the board 1.

At this time, as shown in Figure 5, the resin or solder resist SR is consequently filled from the left side to right side of the via hole 1c by the resultant force  $F$  of vertical component force  $F_y$  and horizontal component force  $F_x$  of the squeegee 20, directed in the downward direction since the squeegee 20 is abutted on the surface of the board 1 under the condition that the end portion of the blade thereof is bent. The air in the via hole 1c is displaced as the squeegee forcefully pushes the resin or solder resist SR and is removed as shown in the drawing.

Next, after the plugging processing of the resin or solder resist SR into the via hole 1c, the mask 10 is removed and the resin or solder resist SR is plugged into the through holes 1d. As described above, because the through holes 1d have a larger diameter than the via holes 1c, accordingly the resin or solder resist SR can be smoothly plugged into the through holes using the conventional above described screen printing method or roller coating method.

Then, light is irradiated onto the board 1 under the condition that the mask 10 capable of exposing the via hole 1c and the through hole 1d is positioned on the upper surface of the board 1 where the resin or solder resist SR is coated and thereby only the exposed portion of the resin or solder resist SR is hardened. The mask 10 is taken away and the printed circuit board is completed by performing a series of post operations for grinding the board 1 with a grinder (not shown) after developing and removing the solder resist SR which is not hardened.

On the other hand, the through hole 1d can also be filled with the resin or solder resist SR by using a mask (not shown) manufactured to expose only the through hole 1d, as in the earlier procedure exposing only the via hole 1c. At this time, it is desirable that the resin or solder resist SR is plugged by abutting the squeegee 20 on the upper surface of the board 1 pushing the resin or solder resist SR.

The screen printing method of positioning a wire woven mesh sieve between the squeegee 20 and the board 1 to plugging holes by positioning the screen having the resin or solder resist or the roller coating method for passing the board between rollers covered with resin or solder resist can also be used for plugging the through holes.

In case of additionally using the mask 10 capable of exposing the through

hole 1, wastage of the resin or solder resist SR in mass production of the printed circuit board can be prevented by blocking the resin or solder resist SR using the mask at the portion of the board where the resin or solder resist SR is not needed to be coated such as in between the via hole 1c and the through hole 1d.

5 The hole plugging method of the printed circuit board in accordance with the present invention will be described with reference to Figures 6 and 7.

In the above embodiment, the resin or solder resist was filled in the via hole or through hole using the squeegee one time but in the following embodiment, the resin or solder resist can be sufficiently plugged in the hole by moving the squeegee at least two times, in case the resin or solder resist is not completely plugged in the hole due to the via hole having a small diameter and a large depth, even though the squeegee is moved one time.

10 Namely, as shown in Figure 6, after positioning the mask 10 for dividing the areas where the resin or solder resist SR is to be coated or not coated on the printed circuit board 1, when the squeegee 20 is directly abutted onto the surface of the printed circuit board 1 as the squeegee 20 is bent a little by virtue of its elasticity and is thereby abutted directly on the upper surface of the surface side circuit pattern 1a.

15 Then, as shown in Figure 6, when a first plugging step is performed by moving the squeegee 20 in the direction of the arrow after coating the resin or solder resist SR on the surface of the printed circuit board 1, the resin or solder resist SR is coated and filled in the spaces between the surface side circuit patterns 1a.

20 At this time, as the squeegee 20 is moved in constant abutted relation to the circuit patterns 1a, the height of the coated resin or solder resist SR plugged in

the spaces 1f between surface side circuit pattern 1a is not protruded higher than the upper surface of the circuit pattern. At this time, in case the entrance side diameter of the via hole 1c is small and the depth is about three times or more deeper than the thickness of the surface side circuit pattern 1a, the resin or solder resist SR may not be sufficiently plugged in the via hole 1c, and only about a half of the hole depth is plugged as shown in Figure 6.

Later, as shown in Figure 7, after re-positioning the squeegee 20 back to the original position, the squeegee 20 is moved to the position as in the first plugging step or moved in the opposite direction to that of the first plugging step and the resin or solder resist SR is completely plugged in the via hole 1c by coating the resin or solder resist one more time.

Then, after removing the mask 10, the resin or solder resist SR is plugged in the through hole 1d as done conventionally, or the resin or solder resist SR is additionally coated on the upper portion of the surface side circuit pattern 1a and the via hole 1c at the same time as the resin or solder resist SR is plugged in the through hole 1d to protect the exposed circuit pattern 1c. Then, drying and grinding processes are performed.

On the other hand, in the step of plugging the via hole operated in two steps, a step of removing air bubbles contained in the resin or solder resist by exposing the printed circuit board under a vacuum condition in the middle of each of the plugging steps can be performed additionally.

In still another embodiment of the present invention, the resin or solder resist SR can be plugged in the through hole after plugging the resin or solder resist in the via hole as in the above embodiment, but the resin or resister SR can be plugged later in the via hole after plugging the resin or solder resist in the

through hole.

Namely, the resin or resister is plugged in the through hole using a silk screen under the condition of exposing the circumferential through hole after masking the via hole formed at the center portion of the printed circuit board and then the resin or solder resist can be plugged without additional masking after removing the mask.

Here, since the squeegee is not abutted on the surface of the printed circuit board in plugging in the through hole, the resin or solder resist is risen around the hole convexly. It is desirable that the resin or solder resist is formed as a flat surface through a flattening processing performed before or after plugging the via hole.

Since the resin or solder resist can be plugged in the via hole or the through hole using the squeegee directly abutted to the board, the resin or solder resist can be plugged in each hole sufficiently and residual air bubbles can be restrained without performing complicated processing such as the vacuum process.

Therefore, damage to the integrated circuit and printed circuit board caused by the air bubbles in mounting or operating the integrated circuit can be prevented and accordingly, reliability can be improved and manufacturing time of the printed circuit board can be reduced, thus to improve productivity.

Particularly, in case the resin or solder resist is coated on the via hole with a small diameter using the screen printing method or the roller coating method, the resin or solder resist is not sufficiently filled since the via hole is covered by the thickness of the screen or the protruded portion of the roller, but in the present invention, the resin or solder resist can be sufficiently filled in each via hole since



the resin or solder resist is plugged by directly abutting the squeegee on the board.

Also, air in the resin or solder resist can be prevented from remaining behind in the hole and the hole and the space in between the circuit patterns by a processing can be plugged at the same time since the resin or solder resist is coated by fixedly-abutting the squeegee to the printed circuit board.

Also, the width and interval of the printed circuit board in accordance with the present invention can be precisely controlled by performing two dimensional gold plating to perform gold plating only on the upper side in plating gold on the pad terminals of the circuit pattern in the wire bonding processing for connecting the circuit pattern of the chip and the printed circuit board by wires in the packaging operation to mount the chip.

In addition, in each embodiment of the present invention, although an insulating material to be plugged was described as limited to the resin or solder resist SR, the descriptions above are just examples and general epoxy resin and solder paste can be used as the insulating material and more generally, materials having viscoelasticity and commonly used in manufacturing the printed circuit board can be used.

As described above, the hole plugging method for the printed circuit boards, and the hole plugging device in accordance therewith can easily plug the insulating material in holes with small diameters and in advance prevent air bubbles from remaining behind in the plugging process by plugging the insulating material in the via hole or through hole by abutting the squeegee on the exposed portion after positioning the mask capable of selectively exposing the portion where the insulating material is to be coated on the board having the via hole and the through hole on the board, thus to prevent damage to the integrated circuit and

the board caused by the air bubbles in mounting or operating the integrated circuit.

Also, other portions other than the circumference of the via hole or the through hole are not coated by the resin or solder resist which is an insulating material and accordingly, waste of the insulating material can be prevented in  
5 mass production.

In case the grinding processing is performed to flatten the surface of the board, since the resin or solder resist SR is not coated at the upper surface of the surface circuit pattern 1a or the amount or coated thickness is small, the grinding process is eased and damage to the surface circuit pattern 1a caused by the  
10 grinding processing can be minimized.

Also, the present invention can enable forming the width and interval precisely by performing two dimensional gold plating by which the upper surface of the pad is plated by gold when gold plating of the pad terminal in the circuit pattern is performed.

15 As the present invention may be embodied in several forms without departing from the spirit or essential characteristics thereof, it should also be understood that the above-described embodiments are not limited by any of the details of the foregoing description, unless otherwise specified, but rather should be construed broadly within its spirit and scope as defined in the appended claims,  
20 and therefore all changes and modifications that fall within the metes and bounds of the claims, or equivalence of such metes and bounds are therefore intended to be embraced by the appended claims.